High-withstand Voltage, 125°C Operation Low Input Offset Voltage



CMOS OPERATIONAL AMPLIFIER for Automotive Use

S-19630A

- A wide operation voltage range (4.0 to 36V) allows the use of S-19630A in a broad range of systems, ranging from 5V to high voltage systems directly connected to battery.
- **a** A low offset voltage of 50μ V max. and low offset voltage drift of 25 nV/°C typ. enable high-accuracy current detection. It greatly contributes to better fuel-efficiency and lower power consumption for automotives.
- Rail-to-rail input enables detection of both low-side and high-side current.

Enables high-accuracy current detection! The S-19630A is a zero-drift amplifier with a wide operation voltage range. A low offset voltage of $50 \mu V$ max. makes it ideal for use in systems that require high-accuracy current detection such as linear solenoids. Application in linear solenoids (low side) Fuel injection system Transmission Control S-19630/ Outputs a voltage according to the amount of current flowing through the shunt resistor Compared to bipolar amplifier characteristics . Bipolar amplifier The use of S-19630A reduces the characteristics dispersion correction process that a system using a bipolar amplifier requires so far.

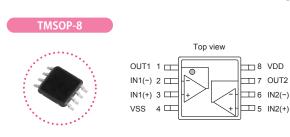
Low offset voltage and low offset voltage drift The offset voltage is approx. 1/100 of that of a bipolar amplifier. Offset voltage 10mV 1mV 100uV 10_uV S-19630A Offset voltage drift (temperature dependency of offset voltage): - Approx. 1/10 of an amplifier whose offset voltage has been corrected by trimming - Approx. 1/100 of a bipolar amplifier 10µV/°C 1uV/℃ 100nV/°C by trimming

Specifications (The listed specifications apply to the whole temperature range.)

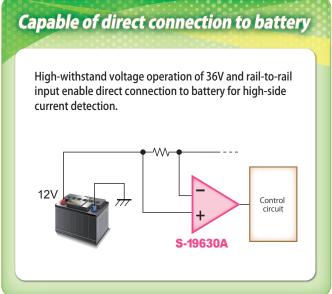
	347
ltem	S-19630AB
Number of circuits	2
Operation power supply voltage range	4.0 to 36.0V
Input offset voltage	\pm 10 μ V typ., \pm 50 μ V max.
Input offset voltage drift	± 25 nV/°C typ., ± 120 nV/°C max., ($V_{DD}=30.0$ V)
Input bias current	10nA max.
Common-mode input voltage range	V_{SS} to V_{DD}
Operation temperature range	Ta=−40 to +125°C (Tj=+150°C max.)
AEC-Q100	In process
PPAP	Capable

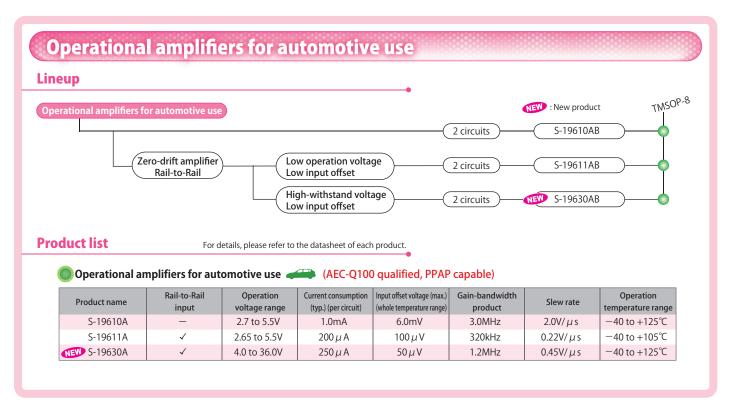
Package

4.0×2.9×t0.8 (max.) mm











ABLIC Inc.

www.ablic.com

Contact us www.ablic.com/en/semicon/sales/

